Docket No.: ASMINT.002C3

## INFORMATION DISCLOSURE STATEMENT

Applicant

Granneman et al. 10 603 515

App. No.

Unknown

Filed

June 24, 2003

For

METHOD AND APPARATUS FOR

SUPPORTING A SEMICONDUCTOR

WAFER DURING PROCESSING

Examiner

Unknown

Group Art Unit

Unknown

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing 1 references that are also enclosed.

This Information Disclosure Statement is being filed within three months of the filing date of this application and no fee is required in accordance with 37 C.F.R. § 1.97(b)(1), (b)(2), or (b)(4).

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

July 29,200

By: Adeel S. Akhtar

Registration No. 41,394

Attorney of Record

Customer No. 20,995

(415) 954-4114

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			U.S. PATENT DOCUMENTS					
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\*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.